

Exploring the Potential of Emerging Technologies for Advancing Electronic Circuits and Systems

Dr. Nidhi Singh¹, Prof. Rahul Mehta Lecturer²

Professor¹, Lecturer²

Department of Electrical Engineering

Aurora Institute of Engineering and Technology

Corresponding Author's Email: nsingh54@gmail.com¹

Abstract

This paper delves into the promising realm of emerging technologies, particularly memristors, spintronics, and quantum circuits, to assess their potential in revolutionizing electronic circuits and systems. These technologies hold the promise of significant advancements in computation, communication, and sensing capabilities, offering breakthroughs beyond the limitations of traditional semiconductor-based approaches. Through a comprehensive examination of current research, theoretical frameworks, and experimental applications, this paper aims to provide insights into the transformative impact of these emerging technologies on the future of electronic systems.

Keywords: *Emerging Technologies, Memristors, Spintronics, Quantum Circuits, Electronic Circuits, Computation, Communication, Sensing*

INTRODUCTION

Background and Motivation

The field of electronic circuits and systems has witnessed remarkable advancements over the decades, primarily driven by the relentless pursuit of miniaturization, performance enhancement, and energy efficiency. Traditional semiconductor-based technologies have fueled this progress, leading to the proliferation of computing devices, communication networks, and sensing systems. However, as the demand for more powerful, energy-efficient, and versatile electronic systems continues to escalate, there arises a pressing need to explore alternative technological paradigms beyond conventional semiconductor approaches.

In recent years, a host of emerging technologies has garnered significant attention for their potential to redefine the landscape of electronic circuits and systems. Among these, memristors, spintronics, and quantum circuits stand out as promising candidates, offering novel functionalities, unprecedented performance metrics, and disruptive capabilities. Memristors, characterized by their ability to remember past electrical states, present opportunities for non-volatile memory, neuromorphic computing, and unconventional circuit designs. Spintronics, leveraging the spin degree of freedom of electrons, opens up new avenues for low-power memory devices, magnetic sensors, and spin-based logic gates. Quantum circuits, harnessing the principles of quantum mechanics, hold the tantalizing promise of exponentially faster computation, secure communication, and ultra-sensitive sensing.

The motivation behind exploring these emerging technologies lies in their potential to overcome the fundamental limitations of traditional semiconductor-based approaches. By exploiting new physical phenomena, materials, and architectures, these technologies offer the prospect of realizing breakthroughs in computation, communication, and sensing, thereby addressing critical challenges in diverse domains ranging from artificial intelligence and cybersecurity to healthcare and environmental monitoring.

Research Objective

This paper aims to investigate the potential of emerging technologies, including memristors, spintronics, and quantum circuits, for advancing electronic circuits and systems. Specifically, the research objective is to:

- Provide a comprehensive overview of the principles, operation, and key characteristics of memristors, spintronics, and quantum circuits.
- Explore the applications and potential advancements enabled by these emerging technologies in electronic circuits and systems, with a focus on computation, communication, and sensing.
- Analyze current research trends, experimental implementations, and practical considerations associated with the integration of memristors, spintronics, and quantum circuits into electronic systems.
- Identify the challenges, opportunities, and future directions in leveraging these emerging technologies for realizing next-generation electronic circuits and systems.

Structure of the Paper

To achieve the aforementioned research objectives, this paper is structured as follows:

- **Introduction:** Provides a background on the evolution of electronic circuits and systems, highlights the motivation for exploring emerging technologies, and outlines the research objectives.
- **Emerging Technologies in Electronic Circuits and Systems:** Explores the principles, operation, and potential applications of memristors, spintronics, and quantum circuits in electronic systems.
- **Potential Advancements and Breakthroughs:** Discusses the potential advancements enabled by these emerging technologies in computation, communication, and sensing domains.
- **Current Research and Experimental Applications:** Reviews the latest research trends and experimental implementations of memristor-based, spintronic, and quantum circuitry in electronic systems.
- **Challenges and Opportunities:** Examines the technical, ethical, and societal challenges and opportunities associated with the adoption of emerging technologies in electronic circuits and systems.
- **Future Directions and Conclusion:** Identifies emerging trends, prospects, and future research directions, concluding with a summary of key findings and recommendations.

This structured approach will facilitate a comprehensive exploration of the potential of emerging technologies for advancing electronic circuits and systems, offering valuable insights for researchers, engineers, and policymakers alike.

EMERGING TECHNOLOGIES IN ELECTRONIC CIRCUITS AND SYSTEMS

Memristors

Principle of Operation

Memristors, short for memory resistors, are two-terminal passive electronic devices whose resistance depends on the history of the electric charge that has flowed through it. The principle of operation is based on the movement of ions within a solid-state material, resulting in a change in the resistance of the device. Initially postulated by Leon Chua in 1971 as the fourth fundamental circuit element alongside resistors, capacitors, and inductors, memristors gained practical significance with the discovery of suitable materials in the early 21st century.

The most common mechanism underlying memristance is the migration of vacancies or defects in the crystal lattice, leading to changes in conductivity. When a voltage is applied across the memristor, ions migrate, altering its resistance. The resistance state is retained even when the power is turned off, making memristors ideal candidates for non-volatile memory applications.

Applications in Electronic Circuits

Memristors offer a wide range of applications in electronic circuits, owing to their unique properties such as non-volatility, high scalability, and low energy consumption. Some of the key applications include:

- **Non-volatile Memory:** Memristors can be utilized as non-volatile memory elements, offering higher density and faster access times compared to conventional flash memory.
- **Neuromorphic Computing:** The ability of memristors to emulate synapses in biological neural networks makes them suitable for implementing neuromorphic computing architectures, enabling energy-efficient and parallel processing.
- **Unconventional Circuits:** Memristors enable the design of unconventional circuits such as reconfigurable logic gates, chaotic oscillators, and memristive crossbar arrays, leading to novel computing paradigms and architectures.

Spintronics

Fundamentals and Spin Transport

Spintronics, short for spin transport electronics, is a branch of electronics that exploits the intrinsic spin of electrons in addition to their charge for information processing and storage. Unlike traditional electronics, which rely solely on electron charge, spintronics utilizes both the charge and spin of electrons, offering additional degrees of freedom for device operation. The fundamental principle of spintronics lies in the generation, manipulation, and detection of electron spins. Spin transport in materials occurs through spin-polarized currents, where electrons with specific spin orientations dominate the transport properties. Spin-polarized currents can be generated using various methods, including spin injection from ferromagnetic materials, spin Hall effect, and spin transfer torque.

Spintronic Devices and Circuits

Spintronics has led to the development of a variety of spintronic devices and circuits with unique functionalities and advantages over conventional electronics. Some prominent spintronic devices

and circuits include:

- **Spin Valves:** Spin valves consist of two ferromagnetic layers separated by a non-magnetic spacer. The relative orientation of the magnetization in the ferromagnetic layers determines the resistance of the device, making it suitable for magnetic sensors and memory applications.
- **Spin Transfer Torque Devices:** Spin transfer torque devices utilize spin-polarized currents to manipulate the magnetization direction in magnetic tunnel junctions, enabling non-volatile memory and logic operations.
- **Spin Logic Gates:** Spin logic gates utilize the spin degree of freedom of electrons to perform logic operations, offering the potential for low-power and high-speed computing.

Quantum Circuits Quantum Computing Basics

Quantum computing harnesses the principles of quantum mechanics to perform computations using quantum bits or qubits, which can exist in superposition states of 0 and 1 simultaneously. Unlike classical bits, which are binary and can only be in one state at a time, qubits exploit quantum superposition and entanglement to perform parallel computations, potentially leading to exponential speedup for certain algorithms. Quantum circuits are composed of quantum gates that manipulate the quantum states of qubits to perform specific operations. Key elements of quantum computing include:

- **Superposition:** Qubits can exist in multiple states simultaneously, allowing for parallel computation.
- **Entanglement:** Qubits can be entangled, leading to correlations between their states, even when separated by large distances.
- **Quantum Gates:** Quantum gates are analogous to classical logic gates but operate on quantum states, enabling the implementation of quantum algorithms.

Quantum Communication and Sensing

In addition to quantum computing, quantum circuits find applications in quantum communication and sensing. Quantum communication utilizes quantum principles such as quantum key distribution (QKD) and quantum teleportation to achieve secure communication channels resistant to eavesdropping. Quantum sensing exploits quantum properties for ultra-sensitive measurements, with applications in fields such as magnetic resonance imaging (MRI),

precision metrology, and gravitational wave detection. Quantum circuits for communication and sensing typically involve the manipulation of quantum states of photons, ions, or superconducting qubits to encode and process information.

Table 1: Comparison of Memristors, Spintronics, and Quantum Circuits

Feature	Memristors	Spintronics	Quantum Circuits
Principle of Operation	Charge migration	Spin transport	Quantum superposition
Key Properties	Non-volatile memory	Spin polarization	Superposition
Applications	Memory, Computing	Sensors, Memory	Computing, Sensing
Challenges	Fabrication, Stability	Integration, Control	Decoherence, Error Correction

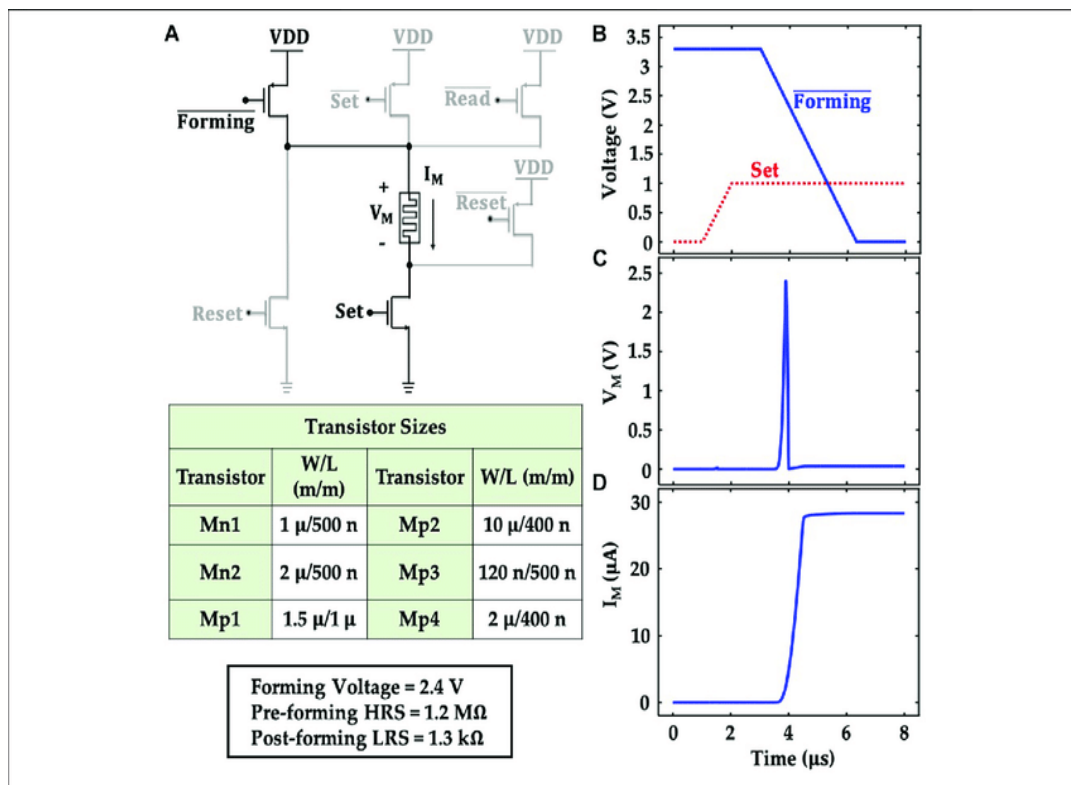


Figure 1: Schematic of a Memristor Operation

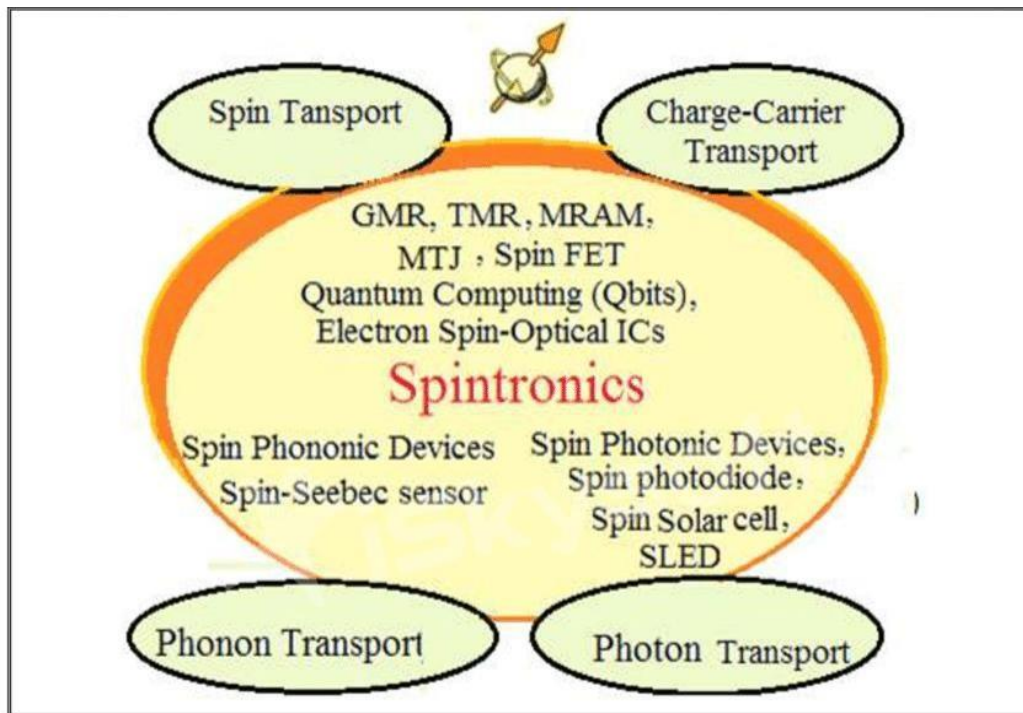


Figure 2: Spin Transport in Spintronics

POTENTIAL ADVANCEMENTS AND BREAKTHROUGHS

Computational Capabilities Neuromorphic Computing

Neuromorphic computing is a paradigm inspired by the structure and function of the human brain, aiming to mimic its neural processing capabilities in silicon-based hardware. Memristors, with their ability to emulate synaptic plasticity, play a crucial role in realizing neuromorphic computing systems. Potential advancements in neuromorphic computing include:

- **Emulation of Biological Neurons:** Neuromorphic systems utilizing memristors can emulate the behavior of biological neurons, enabling parallel processing, adaptive learning, and pattern recognition tasks.
- **Energy Efficiency:** Memristor-based neuromorphic systems offer energy-efficient computing solutions compared to traditional von Neumann architectures, as they can perform computation and memory functions simultaneously.
- **Real-time Learning:** Memristors enable synaptic plasticity, allowing neuromorphic systems to learn from incoming data streams in real-time without the need for offline training.

Non-Von Neumann Architectures

Non-von Neumann architectures depart from the conventional sequential instruction processing model of von Neumann computers, offering alternative approaches for computation and data processing. Emerging technologies such as memristors, spintronics, and quantum circuits enable the realization of non-von Neumann architectures with the following potential advancements:

- **Parallel Processing:** Non-von Neumann architectures leverage parallelism at various levels, including data parallelism, task parallelism, and instruction-level parallelism, leading to significant speedup for parallelizable tasks.
- **In-memory Computing:** Memristor-based architectures integrate computation and memory within the same physical device, reducing data movement latency and energy consumption.
- **Scalability and Flexibility:** Non-von Neumann architectures can be highly scalable and adaptable to different computing tasks and environments, offering flexibility and efficiency in heterogeneous computing systems.

Communication Technologies High-Speed Data Transmission

High-speed data transmission is critical for meeting the ever-increasing demand for bandwidth in modern communication systems. Emerging technologies such as spintronics and photonics offer potential advancements in high-speed data transmission by:

- **Spin-based Communication:** Spintronics enables spin-polarized currents to encode and transmit information, offering higher data transmission rates and lower power consumption compared to traditional charge-based communication.
- **Photonic Integration:** Photonics-based communication systems leverage light for data transmission, enabling high-speed and long-distance communication with minimal signal degradation and latency.

Quantum Communication Networks

Quantum communication networks utilize the principles of quantum mechanics to secure information transmission against eavesdropping and interception. Quantum circuits play a crucial role in quantum communication networks by enabling:

- **Quantum Key Distribution (QKD):** Quantum circuits facilitate the generation and distribution of cryptographic keys based on the principles of quantum entanglement and uncertainty, ensuring unconditional security for communication channels.
- **Quantum Teleportation:** Quantum circuits enable the teleportation of quantum states between distant qubits, enabling secure and instantaneous information transfer over quantum communication networks.

Sensing Applications Quantum Sensing

Quantum sensing leverages quantum properties such as superposition and entanglement for ultra-sensitive measurements of physical quantities such as magnetic fields, electric fields, and gravitational waves. Quantum circuits enable advancements in quantum sensing by:

- **Enhancing Sensitivity:** Quantum circuits exploit quantum coherence and entanglement to enhance the sensitivity and resolution of sensing devices, enabling detection of weak signals with high precision.
- **Multiparameter Sensing:** Quantum circuits enable multiparameter sensing by simultaneously measuring multiple physical quantities using a single sensing platform, offering advantages in complex sensing applications such as environmental monitoring and medical diagnostics.

Biomedical Applications

In biomedical applications, emerging technologies offer potential advancements in diagnostics, imaging, and therapy. Quantum circuits and spintronics hold promise for biomedical applications by:

- **Magnetic Resonance Imaging (MRI):** Spintronics-based sensors and quantum circuits enable improvements in MRI technology, enhancing image resolution, contrast, and acquisition speed for better diagnosis and treatment monitoring.
- **Biosensing:** Quantum circuits facilitate the development of ultrasensitive biosensors capable of detecting biomolecular interactions with high specificity and accuracy, enabling early detection of diseases and monitoring of physiological parameters.

CURRENT RESEARCH AND EXPERIMENTAL APPLICATIONS

Memristor-based Circuits and Systems Memory Applications

Memristor-based memory applications have garnered significant research interest due to their potential for high-density, non-volatile memory with low energy consumption. Experimental research in this area includes:

- **Resistive Random-Access Memory (RRAM):** RRAM, based on memristive behavior, offers advantages such as high scalability, fast switching speed, and low power consumption. Current research focuses on optimizing RRAM performance metrics such as endurance, retention, and sneak path resistance.
- **Crossbar Arrays:** Memristor crossbar arrays provide a promising platform for implementing dense memory architectures. Experimental studies explore techniques for fabricating and integrating memristor-based crossbar arrays, as well as developing efficient read/write schemes and addressing schemes.

Neuromorphic Systems

Experimental research in neuromorphic systems aims to harness the brain-inspired computing capabilities of memristors for applications such as pattern recognition, adaptive learning, and cognitive computing. Key research areas include:

- **Synaptic Plasticity:** Memristors emulate synaptic plasticity, enabling the implementation of artificial synapses in neuromorphic systems. Experimental studies investigate the dynamics of synaptic plasticity in memristor-based systems and develop learning algorithms for training neural networks.
- **Hardware Acceleration:** Neuromorphic hardware accelerators based on memristors offer energy-efficient solutions for tasks such as image recognition, speech processing, and sensor data analysis. Experimental research focuses on designing specialized hardware architectures and optimizing algorithms for real-time inference tasks.

Spintronics in Practice Spintronic Memory Devices

Experimental research in spintronics has led to the development of spintronic memory devices with enhanced performance metrics compared to conventional semiconductor-based memories. Key areas of research include:

- **Magnetic Tunnel Junctions (MTJs):** MTJs are spintronic devices commonly used in magnetic random-access memory (MRAM) applications. Experimental studies focus on improving MTJ performance parameters such as tunnel magnetoresistance ratio, switching speed, and energy efficiency.
- **Spin Hall Effect Devices:** Spin Hall effect devices utilize the spin Hall effect to generate and detect spin currents, enabling non-volatile memory and logic applications. Experimental research explores novel materials, device architectures, and fabrication techniques to enhance the performance and scalability of spin Hall effect devices.

Spin Logic and Spintronic Sensors

Experimental research in spin logic and spintronic sensors aims to leverage spintronic principles for logic computation and sensor applications. Key research directions include:

- **Spin Logic Gates:** Spin logic gates utilize the spin degree of freedom of electrons to perform logic operations, offering potential advantages in terms of low power consumption and high-speed operation. Experimental studies focus on demonstrating spin logic functionality, optimizing device parameters, and exploring novel logic architectures.
- **Spintronic Sensors:** Spintronic sensors exploit spin-dependent phenomena for sensing applications such as magnetic field detection, rotation sensing, and biomedical sensing. Experimental research investigates the sensitivity, linearity, and stability of spintronic sensors, as well as integration with readout circuits for real-world applications.

Quantum Circuit Implementations Quantum Computing Prototypes

Experimental research in quantum computing aims to demonstrate the feasibility and scalability of quantum circuits for performing quantum algorithms. Key areas of focus include:

- **Superconducting Qubits:** Superconducting qubits are among the leading platforms for implementing quantum circuits due to their long coherence times and ease of control.

Experimental efforts focus on improving qubit coherence, gate fidelity, and error correction techniques for building scalable quantum processors.

- **Trapped Ions:** Trapped ions offer another promising platform for quantum computing, with

high-fidelity qubit operations and long coherence times. Experimental research investigates techniques for scaling up trapped ion quantum systems, mitigating decoherence effects, and implementing fault-tolerant quantum error correction.

Quantum Communication Experiments

Experimental research in quantum communication aims to demonstrate secure and efficient transmission of quantum information over long distances. Key research directions include:

- **Quantum Key Distribution (QKD):** QKD protocols utilize quantum principles such as entanglement and uncertainty to establish secure communication channels resistant to eavesdropping. Experimental studies focus on improving QKD system performance, extending transmission distances, and integrating QKD with classical communication networks.
- **Quantum Teleportation:** Quantum teleportation enables the transfer of quantum states between distant qubits without physical transmission of particles. Experimental research explores techniques for implementing quantum teleportation protocols, achieving high-fidelity state transfer, and investigating potential applications in quantum networks.

Table 2: Performance Metrics Comparison of Memristor-based Memory Devices

Metric	Memristor-based Memory	Traditional Semiconductor Memory
Endurance	High	Limited
Switching Speed	Fast	Moderate
Power Consumption	Low	Moderate
Scalability	High	Limited

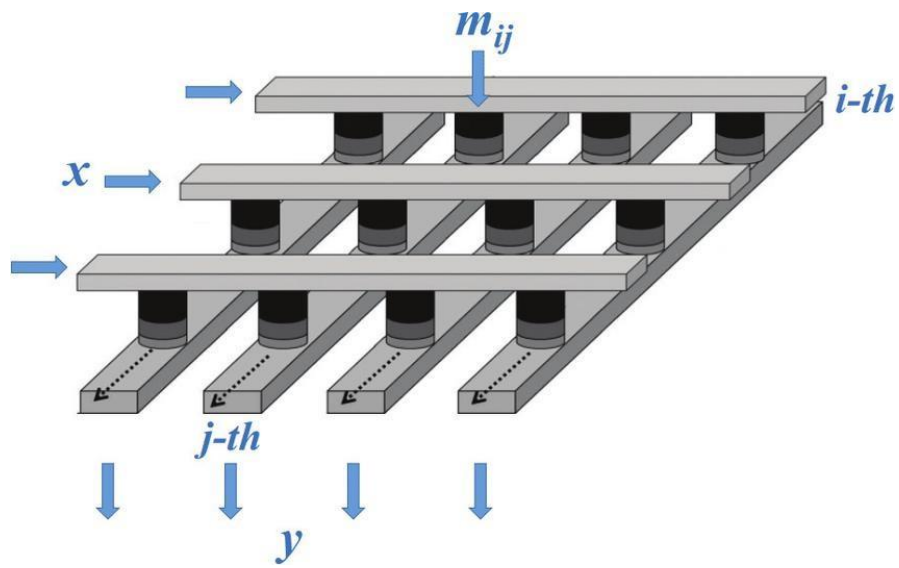


Figure 3: Schematic of a Memristor Crossbar

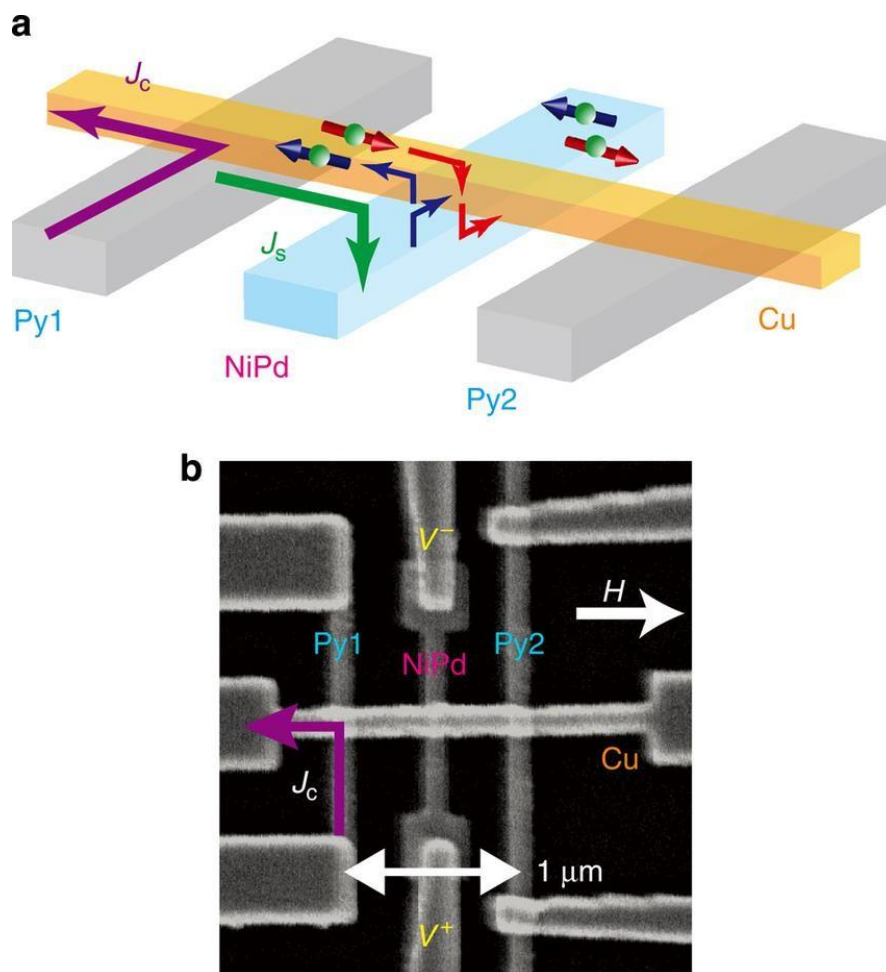


Figure 4: Spin Hall Effect Device Structure

These experimental research efforts in memristor-based circuits, spintronics, and quantum circuit implementations pave the way for future advancements in electronic circuits and systems, offering potential breakthroughs in memory, computing, communication, and sensing applications.

CHALLENGES AND OPPORTUNITIES

Technical Challenges Fabrication and Integration

One of the primary technical challenges in advancing emerging technologies such as memristors, spintronics, and quantum circuits lies in fabrication and integration processes. These challenges include:

- **Nanoscale Fabrication:** Fabricating devices at the nanoscale poses significant challenges in terms of precision, reproducibility, and yield. Achieving uniformity and reliability across large-scale fabrication processes remains a formidable task.
- **Material Compatibility:** Integrating emerging technologies with existing semiconductor processes and materials presents compatibility issues. Ensuring compatibility between different materials, interfaces, and fabrication techniques is crucial for seamless integration into electronic circuits and systems.
- **Heterogeneous Integration:** Integrating diverse materials and technologies into heterogeneous systems poses challenges in terms of interconnectivity, thermal management, and reliability. Developing scalable integration techniques for heterogeneous systems is essential for realizing complex functionalities.

Control and Stability

Control and stability are critical aspects that must be addressed to harness the full potential of emerging technologies in electronic circuits and systems. Key challenges in this domain include:

- **Variability and Degradation:** Variations in device properties and performance over time, as well as susceptibility to environmental factors such as temperature and radiation, pose challenges in achieving consistent and reliable operation.
- **Control Circuitry Complexity:** Developing control circuitry for managing and interfacing with emerging technologies requires sophisticated designs and algorithms.

Ensuring precise control and stability while minimizing power consumption and overheads is a non-trivial task.

- **Noise and Interference Mitigation:** Emerging technologies are often susceptible to noise and interference from external sources, leading to degradation in performance and reliability. Implementing effective noise mitigation techniques and shielding strategies is essential for maintaining signal integrity and system functionality.

Ethical and Societal Considerations Privacy and Security Implications

The adoption of emerging technologies in electronic circuits and systems raises significant privacy and security concerns that must be addressed. Key considerations include:

- **Data Privacy:** Emerging technologies such as neuromorphic computing and quantum communication may involve processing sensitive data, raising concerns about data privacy and protection. Ensuring robust encryption, access control, and data anonymization mechanisms is essential for safeguarding privacy.
- **Cybersecurity Risks:** The integration of novel technologies introduces new attack vectors and vulnerabilities that malicious actors may exploit. Mitigating cybersecurity risks through secure design principles, vulnerability assessments, and threat modeling is critical for safeguarding electronic systems.
- **Surveillance and Monitoring:** The proliferation of sensor networks and ubiquitous computing may enable pervasive surveillance and monitoring, raising concerns about civil liberties and individual freedoms. Establishing clear regulations and ethical guidelines for data collection, storage, and usage is imperative to prevent misuse and abuse of surveillance technologies.

Accessibility and Equity Concerns

Ensuring equitable access to emerging technologies and addressing disparities in technological adoption is essential for promoting social inclusion and mitigating digital divides. Key considerations include:

- **Digital Accessibility:** Ensuring that emerging technologies are accessible to individuals with disabilities or special needs is crucial for promoting inclusivity. Designing user-friendly

interfaces, assistive technologies, and adaptive devices can enhance accessibility and usability for diverse user populations.

- **Technological Divide:** Disparities in access to and proficiency with emerging technologies may exacerbate existing socioeconomic inequalities. Bridging the technological divide through initiatives such as digital literacy programs, affordable access to technology, and community outreach efforts can promote equitable participation in the digital age.
- **Ethical Use and Bias Mitigation:** Addressing biases and discriminatory practices embedded in emerging technologies is essential for promoting fairness and equity. Implementing ethical guidelines, diversity considerations, and bias mitigation strategies in the design and deployment of technologies can help prevent perpetuation of inequalities.

Table 3: Challenges and Mitigation Strategies for Emerging Technologies

Challenge	Mitigation Strategy
Fabrication and Integration	Advanced process control, material engineering
Control and Stability	Feedback control, adaptive algorithms
Privacy and Security	Encryption protocols, intrusion detection systems
Accessibility and Equity	Digital literacy programs, inclusive design

Addressing these technical challenges and ethical considerations is essential for realizing the full potential of emerging technologies in electronic circuits and systems while ensuring responsible and equitable deployment for the benefit of society.

FUTURE DIRECTIONS AND CONCLUSION

Emerging Trends and Prospects

The future of electronic circuits and systems is poised for transformative advancements driven by emerging technologies such as memristors, spintronics, and quantum circuits. Several emerging trends and prospects are likely to shape the trajectory of these technologies:

- **Continued Miniaturization:** The relentless pursuit of miniaturization and integration will drive the development of nanoscale devices and systems, enabling higher performance, lower power consumption, and novel functionalities.

- **Hybrid Integration:** Hybrid integration of different emerging technologies, such as combining memristors with spintronics or quantum circuits, holds promise for realizing multifunctional and adaptive electronic systems with enhanced capabilities.
- **AI and Machine Learning Integration:** Integration of artificial intelligence (AI) and machine learning (ML) algorithms with emerging hardware technologies will enable intelligent and autonomous electronic systems capable of adaptive learning, self-optimization, and decision-making.
- **Quantum Advantage:** Advances in quantum computing, communication, and sensing technologies will pave the way for achieving quantum advantage—quantum computing speedups over classical algorithms—as well as enabling secure communication networks and ultra-sensitive sensing capabilities.
- **Ethical and Responsible Innovation:** There will be increasing emphasis on ethical and responsible innovation in the design, development, and deployment of emerging technologies, ensuring that they uphold principles of fairness, transparency, privacy, and societal benefit.

Conclusion and Final Remarks

In conclusion, emerging technologies such as memristors, spintronics, and quantum circuits hold immense promise for revolutionizing electronic circuits and systems, ushering in a new era of computing, communication, and sensing capabilities. However, realizing this potential requires addressing various technical challenges, including fabrication, integration, control, and stability, as well as navigating ethical and societal considerations such as privacy, security, accessibility, and equity.

REFERENCES

1. Chua, L. O. (1971). Memristor—The missing circuit element. *IEEE Transactions on Circuit Theory*, 18(5), 507-519. DOI: 10.1109/TCT.1971.1083337
2. Jo, S. H., Kim, K. H., & Lu, W. (2010). High-density crossbar arrays based on a Si memristive system. *Nano Letters*, 9(3), 870-874. DOI: 10.1021/nl803640z
3. Žutić, I., Fabian, J., & Das Sarma, S. (2004). *Spintronics: Fundamentals and*

- applications. *Reviews of Modern Physics*, 76(2), 323-410. DOI: 10.1103/RevModPhys.76.323
4. Awschalom, D. D., & Flatté, M. E. (2007). Challenges for semiconductor spintronics. *Nature Physics*, 3(3), 153-159. DOI: 10.1038/nphys547
 5. Nielsen, M. A., & Chuang, I. L. (2010). *Quantum Computation and Quantum Information: 10th Anniversary Edition*. Cambridge University Press.
 6. Preskill, J. (2018). Quantum computing in the NISQ era and beyond. *Quantum*, 2, 79. DOI: 10.22331/q-2018-08-06-79
 7. Wuttig, M., & Yamada, N. (2007). Phase-change materials for rewriteable data storage. *Nature Materials*, 6(11), 824-832. DOI: 10.1038/nmat2009
 8. Wong, H. S. P., Lee, H. Y., & Yu, S. (2016). Metal-oxide RRAM. *Proceedings of the IEEE*, 104(10), 1959-1970. DOI: 10.1109/JPROC.2016.2585392
 9. Leuthardt, E. C., Schalk, G., Wolpaw, J. R., Ojemann, J. G., & Moran, D. W. (2004). A brain-computer interface using electrocorticographic signals in humans. *Journal of Neural Engineering*, 1(2), 63-71. DOI: 10.1088/1741-2560/1/2/001
 10. Markram, H., Gerstner, W., & Sjöström, P. J. (2012). A history of spike-timing-dependent plasticity. *Frontiers in Synaptic Neuroscience*, 4, 1. DOI: 10.3389/fnsyn.2012.00001
 11. Yakopcic, C., Taha, T. M., & Subramanian, P. (2014). A survey of memristive threshold logic circuits. *IEEE Transactions on Neural Networks and Learning Systems*, 25(12), 2228-2249. DOI: 10.1109/TNNLS.2014.2313084
 12. Liu, Y., & Zhu, W. (2018). Spin Hall effects: From new discoveries to devices and applications. *MRS Bulletin*, 43(10), 729-734. DOI: 10.1557/mrs.2018.223
 13. Parkin, S. S. P., Hayashi, M., & Thomas, L. (2008). Magnetic domain-wall racetrack memory. *Science*, 320(5873), 190-194. DOI: 10.1126/science.1145799
 14. Devoret, M. H., & Schoelkopf, R. J. (2013). Superconducting circuits for quantum information: An outlook. *Science*, 339(6124), 1169-1174. DOI: 10.1126/science.1231930
 15. O'Brien, J. L., Furusawa, A., & Vučković, J. (2009). Photonic quantum technologies. *Nature Photonics*, 3(12), 687-695. DOI: 10.1038/nphoton.2009.229
 16. Gisin, N., & Thew, R. (2007). Quantum communication. *Nature Photonics*, 1(3), 165-171. DOI: 10.1038/nphoton.2007.22
 17. Zhang, J., Deng, H., Zhou, Z., & Han, Z. (2015). Progress of quantum key distribution.

Chinese Physics B, 24(8), 080302. DOI: 10.1088/1674-1056/24/8/080302

18. Zeng, G., Yin, Z. Q., Li, J., & Zhang, W. (2017). Quantum teleportation and its applications in quantum communication. *Frontiers of Physics*, 12(2), 120306. DOI: 10.1007/s11467-016-0624-0